

# Abstracts

## A GaAs MESFET 7 Gb/s Dynamic Decision Circuit I.C.

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*R.J. Bayruns, A.D.M. Chen, J. Gilbert, R. Goyal and J. Wang. "A GaAs MESFET 7 Gb/s Dynamic Decision Circuit I.C.." 1988 *Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 88.1* (1988 [MCS]): 27-30.*

A dynamic decision circuit uses 0.5  $\mu$ m gate GaAs MESFET technology and charge canceling techniques to obtain a 7 Gb/s clocking rate. The 0.6Vp-p output data eye is symmetrical and has 70 pS rise and fall times while retiming a noisy data input. The circuit operates from a single -6V supply and dissipates 0.24W of power. The chip size is 0.5 mm<sup>2</sup>.

[Return to main document.](#)